AMENDMENTS TO THE SPECIFICATION

In the Detailed Description of the present application, please replace the paragraph beginning on line 11 of page 5 with the following new paragraph:

Within the multiplexing module 102 of Figure 1, configuration routing 110 currently couples mid-bus 104 with mid-bus 114 while configuration routing 112 currently couples mid-bus 106 with mid-bus 120. In this manner, adapter chip 122 is coupled to utilize mid-bus 104 while adapter chip 124 is coupled to utilize mid-bus 106. Within bus configuration multiplexer system 100, controller adapter chip 122 is coupled with add-in I/O card slot 130 by card bus 126 while controller adapter chip 124 is coupled with add-in I/O card slot 132 by card bus 128. As such, add-in I/O card slot 130 is currently coupled with mid-bus 104 while add-in I/O card slot 132 is coupled with mid-bus 106. In this manner, an interface card coupled with add-in I/O card slot 130 can communicate with other computer system components (not shown) via mid-bus 104 while another interface card coupled with add-in I/O card slot 132 can communicate with other computer system components via mid-bus 106.

In the Detailed Description of the present application, please replace the paragraph beginning on line 9 of page 15 with the following new paragraph:

Within Figure 4, the mid-bus 404 is coupled with controller adapter chip 418 which is coupled with add-in I/O card slot 426 by card bus 422. It is noted that card bus 422 can be implemented to handle a communication bandwidth equal to or greater than the combined communication bandwidth of the mid-buses 404 and 406. The mid-bus 408 is coupled with controller adapter chip 420 which is coupled with add-in I/O card slot 428 by card bus 424. The card bus 424 can be implemented to handle a communication bandwidth equal to or greater than the combined communication bandwidth of the mid-buses 406 and 408. The mid-bus 406 is coupled with multiplexing module 402. Additionally, the multiplexing module 402 is coupled with controller adapter chip 418 via mid-bus 414. Also, the multiplexing module 402 is coupled with controller adapter chip 420 via mid-bus 416. The configuration module 410 is coupled with the multiplexing module 402 and is capable of controlling the routing configuration functionality of the multiplexing module 402.